



PTO/SB/08B, (10-01)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 2 of 2

**Complete if Known**

|                        |                  |
|------------------------|------------------|
| Application Number     | 10/080,950       |
| Filing Date            | February 20 2002 |
| First Named Inventor   | Reid R. Harrison |
| Group Art Unit         | 2817             |
| Examiner Name          | Unassigned       |
| Attorney Docket Number | 40022.002        |

**OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

| Examiner Initials* | Cite No. 1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|--------------------|------------|---|----------------|
| SJ                 |            | DEGRAUWE et al., "A Micropower CMOS-Instrumentation Amplifier", IEEE J. Solid-State Circuits, Volume 20 (1985), pages 805-807   |                |
| SJ                 |            | VAN PETEGHEM et al., "Micropower High-Performance SC Building Block For Integrated Low-Level Signal Processing", IEEE J. Solid State Circuits, Volume 20 (1985), pages 837-844  |                |
|                    |            | DORMAN et al., "A Monolithic Signal Processor For a Neurophysiological Telemetry System", IEEE J. Solid State Circuits, Volume 20 (1985), pages 1185-1193   | X              |
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| SJ                 |            | METTING VAN RIJN et al., "High-Quality Recording of Bioelectric Events", Medicine and Biology, Engineering and Computers, Volume 29 (1986), pages 1035-1044   |                |
| SJ                 |            | JI et al., "An Implantable CMOS Circuit Interface For Multiplexed Microelectrode Recording Arrays", IEEE J. Solid-State Circuits, Volume 27 (1992), pages 433-443   |                |
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| SJ                 |            | MARTINS et al., "A CMOS IC For Portable EEG Acquisition Systems", IEEE Trans. Instrument and Measurement, Volume 47 (1998), pages 1191-1196   |                |
| SJ                 |            | STEYAERT et al., "Low-Noise Monolithic Amplifier Design: Bipolar Versus CMOS", Analog Integrated Circuits and Signal Processing, Volume 1 (1991), pages 9-19  |                |
|                    |            |   |                |
|                    |            |   |                |

Examiner Signature

S J Mottoke

Date Considered

4-3-03

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|  |   |   |   | <b>First Named Inventor</b> | Reid R. Harrison  |
|  |   |   |   | <b>Art Unit</b>             | 2817              |
|  |   |   |   | <b>Examiner Name</b>        | Unassigned        |
| Sheet  | 1 | o | 2 | Attorney Docket Number      |                   |

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

|                       |             |                    |        |
|-----------------------|-------------|--------------------|--------|
| Examiner<br>Signature | S J Nottola | Date<br>Considered | 4-3-03 |
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